

# Medium Voltage Variable Frequency Drive with Active Front End (AFE)

Rockwell Automation has advanced power conversion technology with Current Source topology using an active front-end, called a PWM (Pulse Width Modulated) Rectifier. This is a particularly attractive option for applications with new motors since it does not require an isolation transformer to meet IEEE-519 and EN61000-2-4 harmonic standards. Most available technologies in today's MV drive market require a multi-winding transformer to mitigate the unwanted harmonics through cancellation by phase shifting the transformer secondary windings.

The PWM rectifier requires a simple three phase input from the customer power supply. While an isolation transformer is not normally required, it can be configured to reduce the higher system voltage to operating voltage level based on the inverter and motor operation voltage. A simplified electrical diagram shows this type of configuration for a 4160 volt drive with Active Front End rectifier. Line reactors are normally used if the supply voltage meets the operational drive voltage and if new motors are supplied.

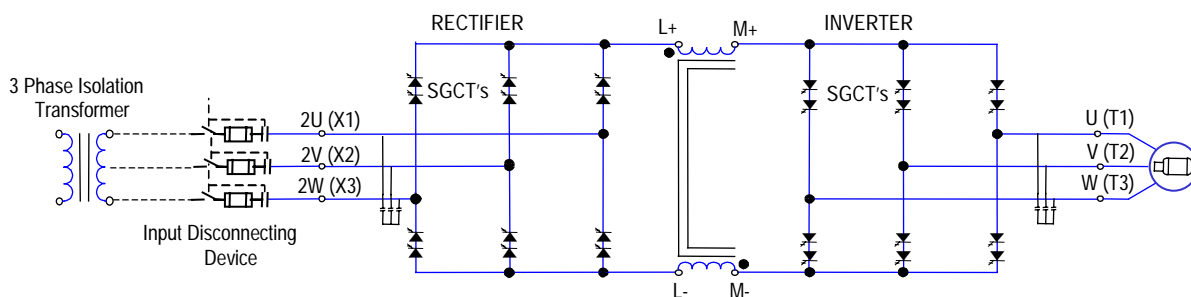


Figure 1 – 4160 Volt Drive with Isolation Transformer and Active Front End Rectifier

The Active Front End rectifier switching pattern uses a similar control algorithm to the inverter. The pattern used is a 7-pulse selective harmonic elimination (SHE) pattern, which eliminates the 5<sup>th</sup>, 7<sup>th</sup> and 11<sup>th</sup> harmonics. Figure 2 shows the typical rectifier switching pattern with 7-pulse with a single phase representation. This switching pattern is repeated for all three phase shifting 120 electrical degrees. The duration of the on state time will then determine the controlled rectifier voltage to the dc link.

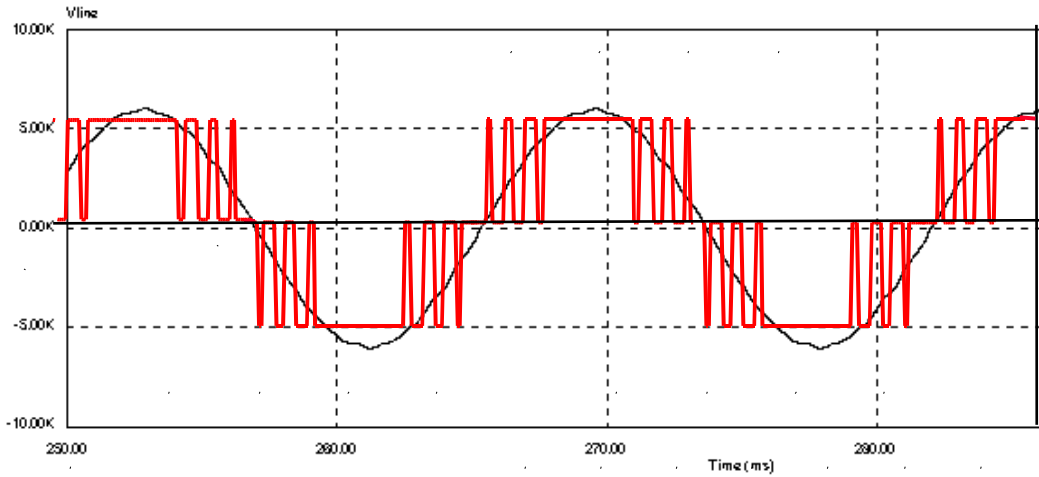


Figure 2 – Typical Active Front End Rectifier Switching Pattern

The drive has a defined firing and applies the above firing pattern to three phases. The drive rectifier has the following firing order: 1, 2, 3, 4, 5, and 6 with the bridge hardware correct hardware configuration as outlined in Figure 3. As shown the 4160 V rectifier uses two ( 2 ) - 6500 volt power devices in series to achieve the proper electrical peak inverse blocking margin of 13000 volts for each leg.

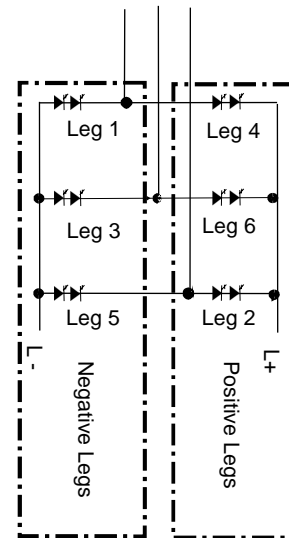
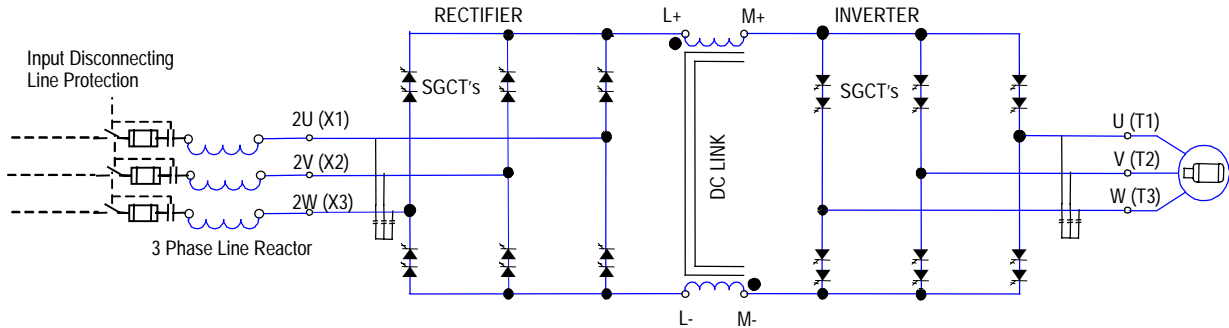


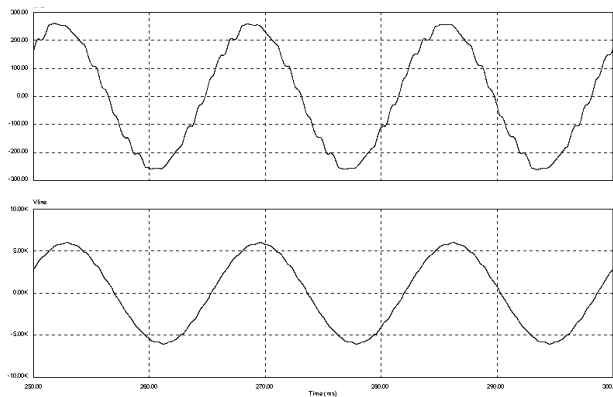
Figure 3 – 4160 V Rectifier

The input capacitors connected at the input with the rectifier are designed to reduce the higher frequency current harmonics. The inductance/capacitor resonant frequency is placed below 300 Hz where no characteristic harmonics exist. This prevents the excitation of system harmonic frequencies. The capacitor is selected such that a high input power factor is achieved. In most cases a line reactor can be used to provide the required design impedance, giving additional current limiting features to a line side short circuit fault. When operating from a power source that does not require voltage matching to the drive rating, a simple line reactor configuration can be adopted. (See Figure 4)



**Figure 4 – 4160 Volt Drive with Line Reactor and Active Front End Rectifier**

The rectifier input current and voltage waveforms are shown in Figure 5. A current Total Harmonic Distortion (THD) of approximately 5.0 % is achieved. The voltage distortion is based on the customer's maximum short circuit rating ( $I_{SC}$ ) and maximum demand load current ( $I_L$ ). With a ratio of 20, the voltage distortion will be less than 3%.



**Figure 5 – Active Front End Line Input Waveforms**

(Top to bottom: line current and line-to-line voltage at the input of the drive).

Comparison of Rectifier Types

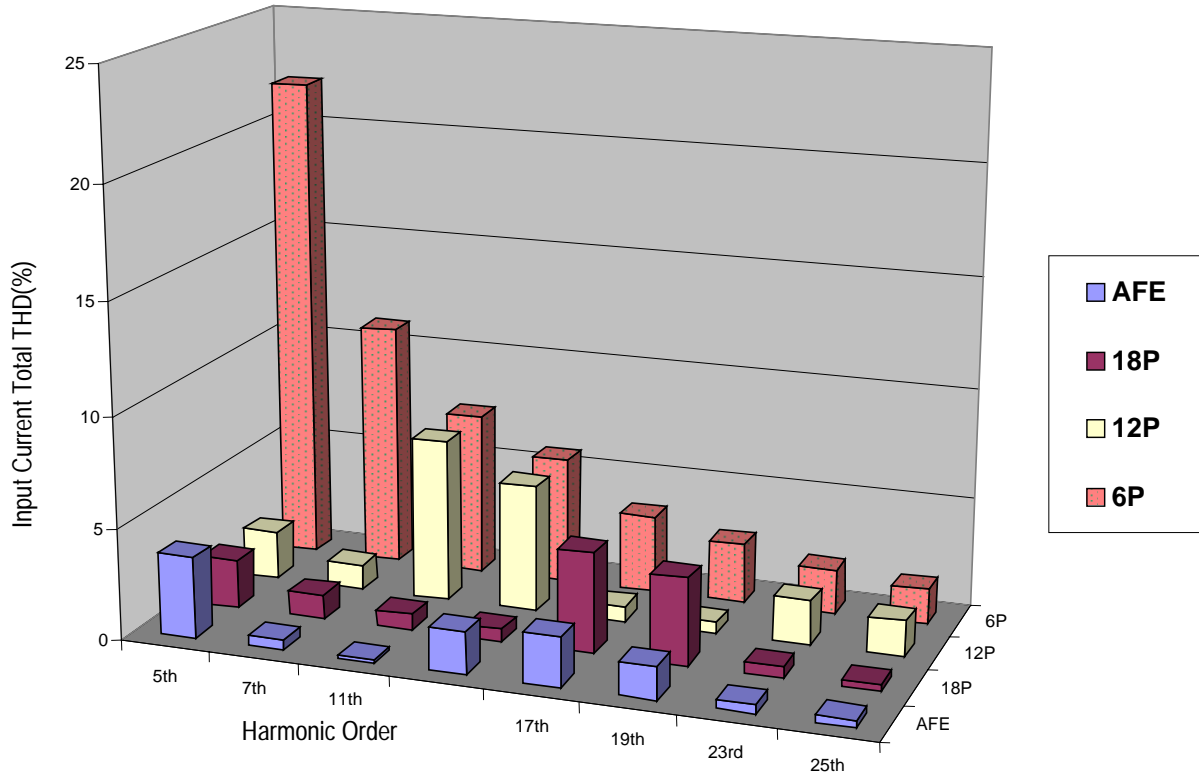


Figure 6 - Input Current Total THD (%) for different rectifier types

Rectifier Type	Total THD
AFE	4-5%
18 Pulse	5-6%
12 Pulse	8-11%
6 Pulse	25-27%







